

Docket No.: 67161-117

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Shinji TANAKA	:	Confirmation Number:
	:	
Serial No.:	:	Group Art Unit:
	:	
Filed: October 10, 2003	:	Examiner: Unknown
	:	
For:		SEMICONDUCTOR MEMORY DEVICE INPUTTING/OUTPUTTING DATA AND PARITY DATA IN BURST OPERATION

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

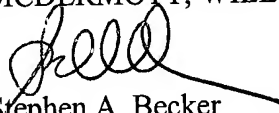
The relevance of Japanese Patent Laying-Open No. 11-65944 is discussed in the present specification.

**Serial No.:**

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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**Date: October 10, 2003**

<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>				ATTY. DOCKET NO. <b>67161-117</b>		SERIAL NO.		
(PTO-1449)				APPLICANT <b>Shinji TANAKA</b>				
				FILING DATE <b>October 10, 2003</b>		GROUP		
<b>U.S. PATENT DOCUMENTS</b>								
EXAMINER'S INITIALS	CITE NO.	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		Number-Kind Code <sup>2</sup> (if known)						
		US						
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<b>FOREIGN PATENT DOCUMENTS</b>								
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
		Country Codes-Number 4-Kind Codes (if known)					Yes	No
		JP 11-65944		03/09/1999	FUJITSU LTD		(Japan w/English Abstract)	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.						
EXAMINER				DATE CONSIDERED				

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.